

Substitute for Form 1449/PTO

AUG 1 2016

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

<i>Complete If Known</i>	
Application Number	09/955,529
Filing Date	September 17, 2001
First Named Inventor:	Mark Peting
Art Unit	2631
Examiner Name	Not Yet Assigned
Attorney Docket Number	004250 P032

Sheet 1 of

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No.*	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures or Tables
		Number-Kind Code ² (if known)			RECEIVED
	us-	4,015,238	3/29/1977	Robert Curtis Davis	
	us-	5,027,374	6/25/1991	Mark W. Roszman	AUG 1 3 2004
	us-	5,291,499 A	3/1/1994	Behrens et al.	
	us-	5,295,142	3/15/1994	Izumi Hatakeyama	
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	us-				

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Figure Arose

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FOREIGN PATENT DOCUMENTS

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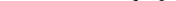


Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

Substitute for Form 1449A/PTO				<i>Complete If Known</i>	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT				Application Number	09/955,529
				Filing Date	September 17, 2001
				First Named Inventor	Mark Petting
				Art Unit	2631
				Examiner Name	Unassigned
Sheet	2	of	2	Attorney Docket Number	42599P032

NON-PATENT LITERATURE DOCUMENTS

Examiner Signature		Date Considered	4/28/05
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Based on PTO/SB/088 (08-03) as modified by Blakely, Solokoff, Taylor & Zafman (wir) 08/11/2003.
Send To: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450



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Information Disclosure Statement by Applicant <i>(use as many sheets as necessary)</i>		<i>Complete if Known</i>	
Sheet	1	of	2
		Application Number	09/955,529
		Filing Date	September 17, 2001
		First Named Inventor	Mark Petting
		Art Unit	2631
		Examiner Name	Unassigned
		Attorney Docket Number	AS50D022

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FOREIGN PATENT DOCUMENTS

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[Applicant's unique classification designation number (optional).] See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. [Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3).] For Japanese patent documents, the indication of the year of reign of the Emperor must precede the serial number of the patent document. [Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible.] [Applicant is to place a check mark

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Sheet	2	of	2	Attorney Docket Number	004259.P03
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NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²
CW		BREE, ET AL., "A Bit-Serial Architecture For A VLSI Viterbi Processor", Communications Systems Research Group, University of Saskatchewan, Saskatoon, IEEE, WESCANEX '88, 1988, pages 72-77.	
CW		BIVER, ET AL., "Architectural Design and Realization Of A Single-Chip Viterbi Decoder", Elsevier Science Publishers B.V., INTEGRATION, The VLSI Journal 8 (1989), October, No. 1, Amsterdam, NL, Pages 3-16.	
CW		BREE, ET AL., "A Modular Bit-Serial Architecture For Large Constraint-Length Viterbi Decoding", Communications Systems Research Group, University of Saskatchewan, Saskatoon, Canada, IEEE International Conference on Communications', 1990, pages 1501-1506.	
CW		CHOI, ET AL., "Viterbi Detector Architecture For High-Speed Optical Storage", 1997, IEEE TENCON - Speech and Image Technologies for Computing and Telecommunications, ASIC Center Corporate Technical Operations SAMSUNG Electronics, Vol. 1, Dec 1997, pages 89-92.	
CW		W.H. YIM and F.P. COAKLEY, "On-Board Processing For KA-Band Applications", University of Surrey, UK, Publication Date, February 11, 1993, XP 000458011, pp. 225-229.	
CW		HASHIDA-MITSUYOSHI, "Hierachical Network Management System and Control Method for Network Management Information", Patent Abstracts of Japan, Publication No. 07220777	

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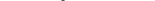
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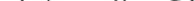
FOREIGN PATENT DOCUMENTS

FOREIGN PATENT DOCUMENTS							
Exam. Initial*	Cite No. ¹	Foreign Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD- YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T6
<i>ca</i>	EP	0 762 777		Uchiumi, Tadasugu, et al.	3-12-1997		

Examiner Signature  Date Considered 1/28/05

Unique citation designation number. See attached *Keys of U.S. Patent Documents*. Enter Office that issued the document by the two-letter code (WIPO Standard S-2). For Japanese patent documents, the indication of the year of reign of the Emperor must precede the serial number of the patent document. Kind of document by the appropriate symbol as indicated on the document under WIPO Standard ST.16 (Kanshi-ji). Add patent/la/standard/works/rack/nom if French/Japanese Translation is attached.

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Substitute for Form 1449A/PTO (Modified) (use as many sheets as necessary)		Attorney Docket No.: 04259.P032	Application Number: 09/955,529
 MAR 05 2002 SAC		First Named Inventor: Mark Petting	RECEIVED
		Filing Date: September 17, 2001	MAR 1 2002
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OTHER ART - NO PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	Translation ²
<i>CR</i>		JAMES TSUI, Frequency Channelization, Digital Techniques for Wideband Receivers, Second Edition, Pages 363 - 396, 2001 Artech House, Inc., Norwood, MA	
<i>CR</i>		ZHENGDAO WANG and GEORGIOS B. GIANNAKIS, Wireless Multicarrier Communications where Fourier Meets Shannon, Department of ECE, University of Minnesota, Minneapolis MN, Pages 1-21	
<i>CR</i>		E. VERRIEST, ISEN, Implementing an Adaptive Noise Canceling System to Enhance Sonar Receiver Performance Using the TMS320C31 DSP, ESIEE, Paris, September 1996, Texas Instruments, Pages 1-24.	
<i>CR</i>		G.A. SHAW, R.A. FORD, J.C. ANDERSON, B.W. ZUERDNORFER, A.H. ANDERSON, RASSP Benchmark 2 Technical Description, Massachusetts Institute Of Technology Lincoln Library, 153 pages total.	
<i>CR</i>		"www.inventra.com/inventra/softcore/workshop/MultiRaFiltDes95/", MENTOR GRAPHICS, Hardware Design of Decimators/Interpolators, Pages 1-38.	
<i>CR</i>		"www.mentor.com/inventra/softcore/workshop/SDmod95/", MENTOR GRAPHICS, Introduction to AD/DA Converters, Pages 1-27.	
<i>CR</i>		http://www.mentor.com/inventra/softcore/workshop/SDHWDes95/ MENTOR GRAPHICS, Design of the Decimation & Interpolation Filters, Pages 1-57.	
<i>CR</i>		http://www.mentor.com/inventra/softcore/workshop/Applications95/, MENTOR GRAPHICS, Sigma-Delta Converter Applications, Pages 1-5.	

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